

Fig. 1

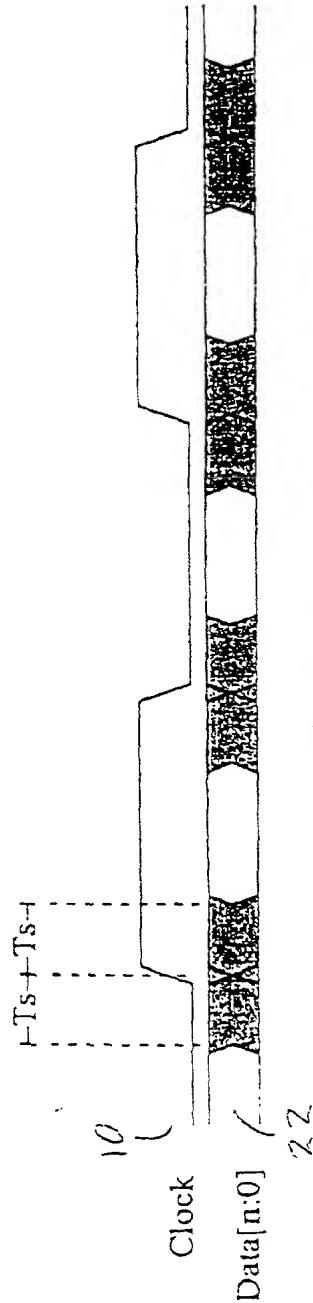


Fig. 2

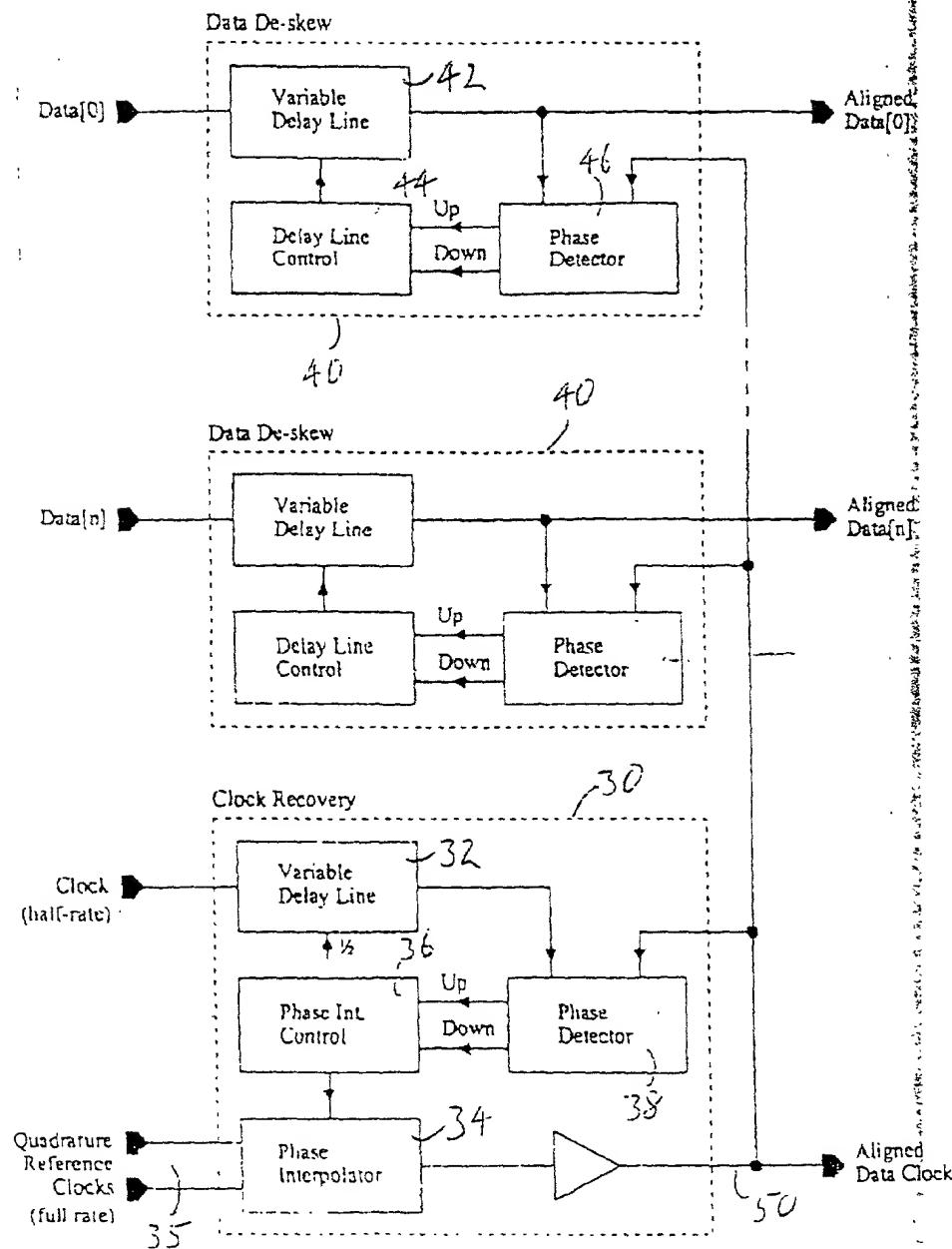
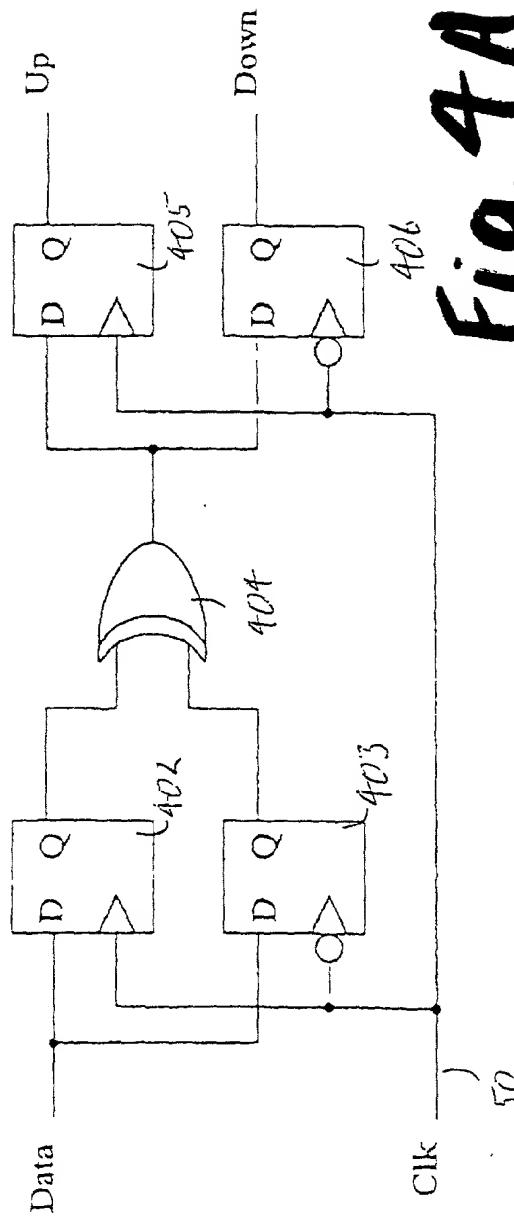
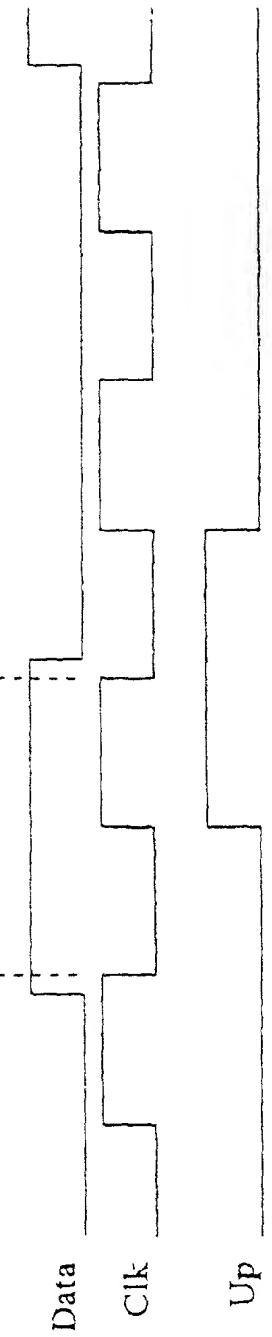


Fig. 3

Fig. 4AFig. 4B

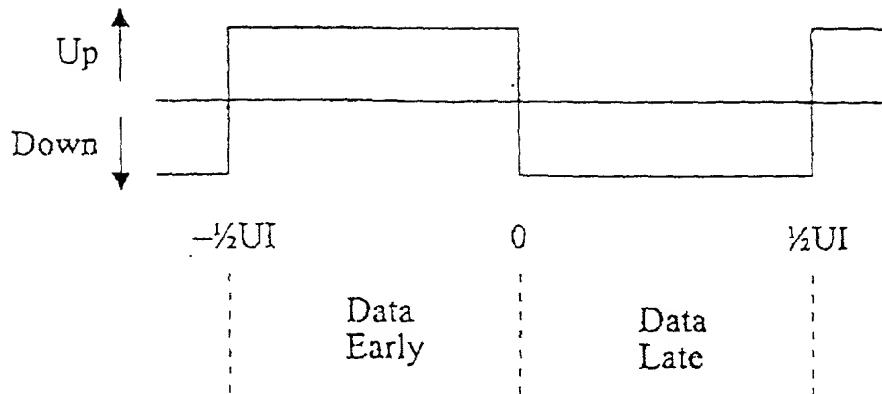


Fig. 5

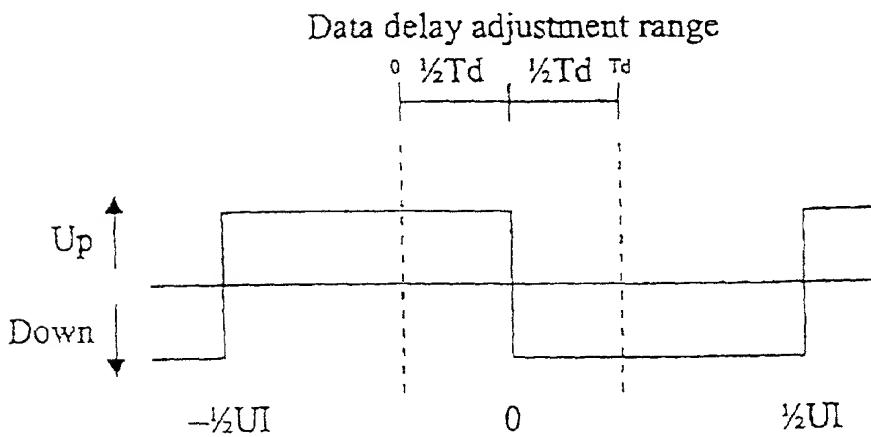
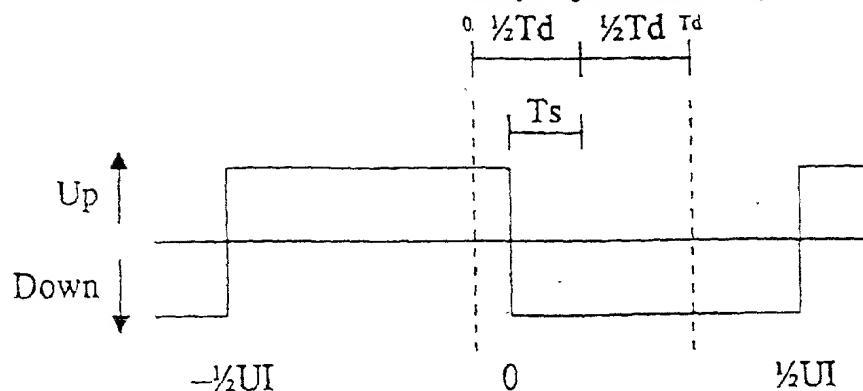
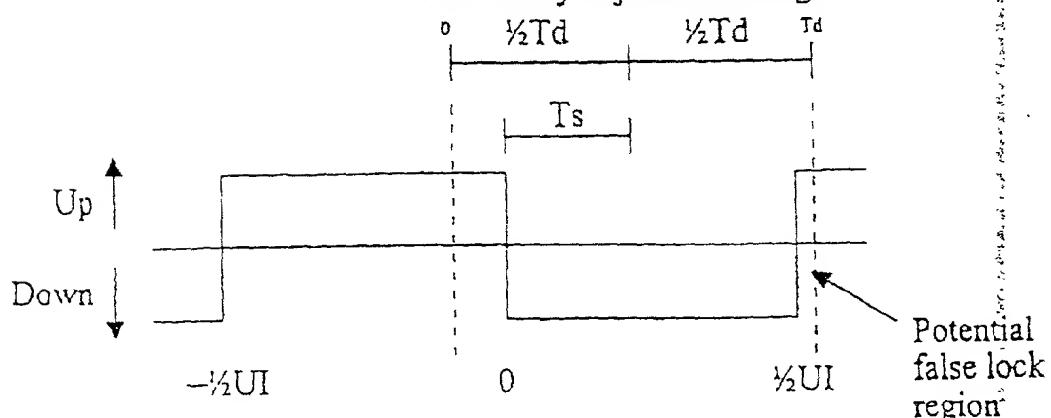


Fig. 6

Data delay adjustment range

Fig. 7

Data delay adjustment range

Fig. 8

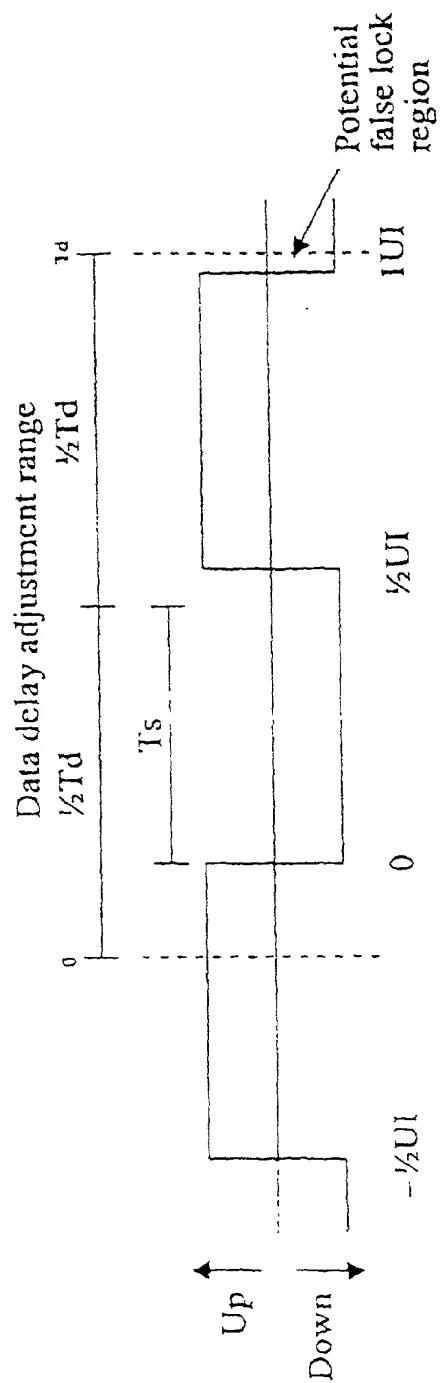


Fig. 9

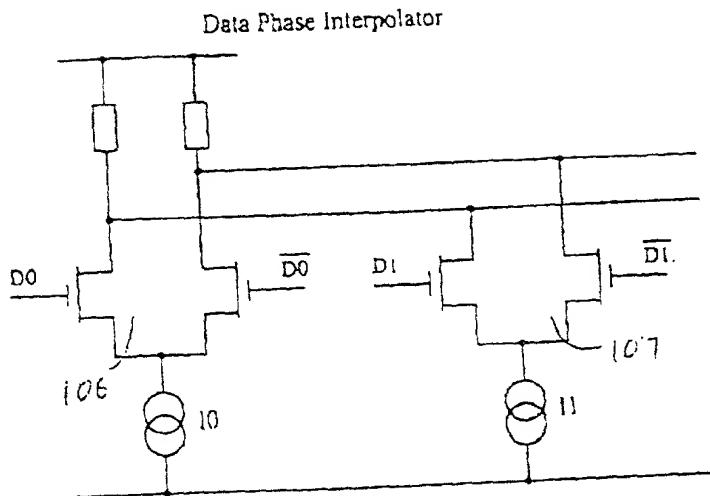
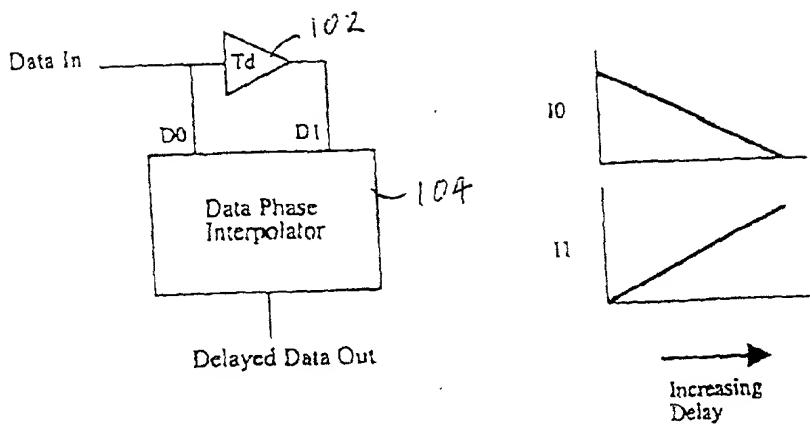
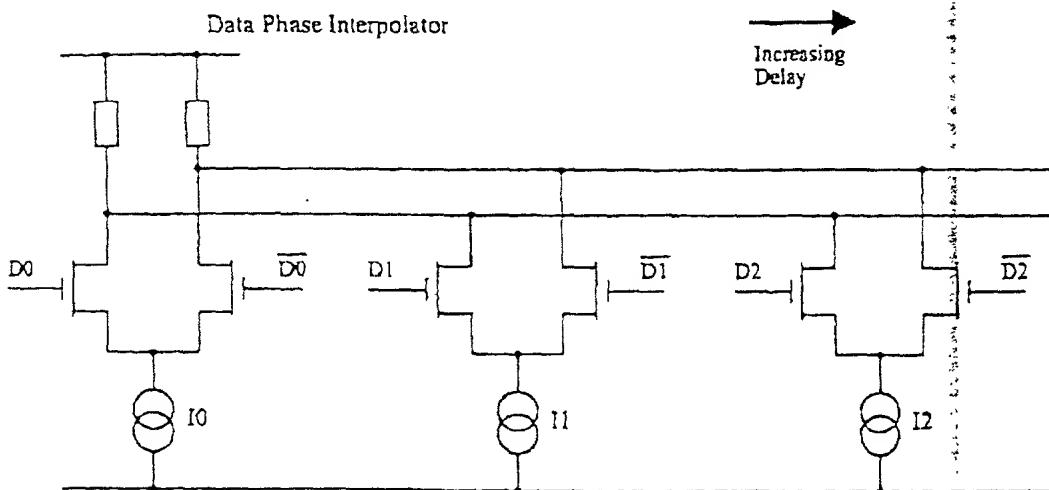
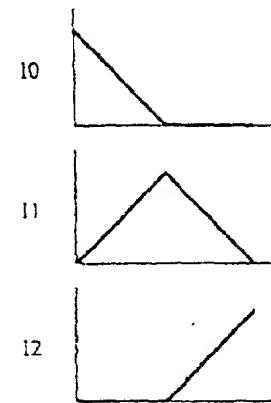
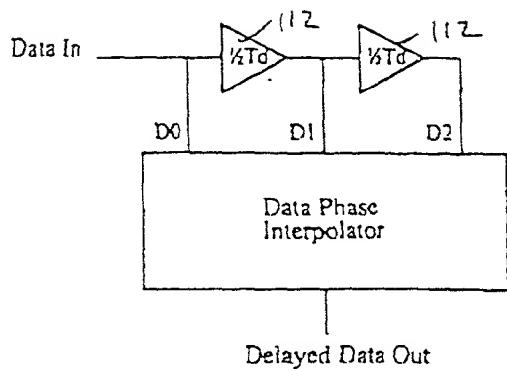


Fig. 10



Increasing
Delay

Fig. 11